

AMENDMENTS TO THE SPECIFICATION

Please amend Paragraph 0017 on page 5 of the Specification as set forth below:

[0017] The outputs of the weak and strong stages 22 and 24 are commonly coupled to the input of the driver 20. The combined output signals of the stages 22 and 24 drive the low-to-high (LH) and high-to-low (HL) signal transitions of the driver 20. More specifically, as shown in FIG. 3, the input of the driver 20 has a threshold voltage V_{TN} (switch level) which triggers signal transitions of its output signal. The waveform PREDRVOUT, generated by the weak and strong stages 22 and 24, triggers a low-to-high (LH) transition of the driver's output signal after a LH switching delay period t_{SWLH} and triggers a high-to-low (HL) transition of the driver's output signal after a HL switching delay period t_{SWHL} . The signal switching delay time periods t_{SWLH} and t_{SWHL} are defined in the Background Section. It should be noted that in FIGS. 1 and 3, the LH and HL designations are taken with respect to the output of the driver 20 and not with respect to the transitions of the waveform PREDRVOUT.